EV318284442

Inventor:

Janos Fucsko et al.

Title:

Methods of Etching Silicon Nitride Substantially Selectively Relative

to an Oxide of Aluminum and Methods of Forming Trench Isolation

Within a Semiconductor Substrate

Assignee: Micron Technology, Inc.

## INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 18 Sep 2003

Attorney:

Reg. No. 44,854

Form PTO-1449

## U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

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U.S. PATENT DOCUMENTS														
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<u> </u>	AM		Deckert, Pattern Etching of CVD Si₃N₄/SiO₂ Composites in HF/Glycerol Mixtures,											
				127 J. ELECTROCHEM. Soc., No. 11, pp. 2433-2438 (November 1980).										
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